

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

USAMI, Tatsuya

Serial No.: 09/851,313

Group Art Unit: 2823

Filing Date: May 9, 2001

Examiner: Maldonado, Julio J.

For: SEMICONDUCTOR DEVICES, SEMICONDUCTOR WAFER AND METHODS  
OF PRODUCING THE SAME DEVICE AND WAFER

Assistant Commissioner of Patents  
Alexandria, VA 22313-1450  
Box AF

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Under the provisions of 37 CFR §1.97 through §1.98 and pursuant to Applicant's duty of disclosure under 37 CFR §1.56, Applicant respectfully brings the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. All of these references were cited by the Examiner in the co-pending Application No. 10/135,393, in an Office Action dated October 28, 2005.

I hereby certify that no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in §1.56(c) more than three months prior to the filing of the information disclosure statement.

Submitted herewith is the IDS fee of \$180.00.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Date: 11/21/05  
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Respectfully submitted,

  
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